

**P.E.S.College of Engineering, Mandya-571401**

(An Autonomous Institution Under VTU. Belgaum)

**Department of Electronics & Communication Engg.**

**TIME – TABLE ODD SEM: (Reopening: 25-07-2016 Ending : -11-2016)**

**Semester: III      Section: A      Class Room: ECL- 305      Month: July      Year/: 2016**

Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
	MON				1	T E A  B R E A K  L U N C H  T I M E	3		4	2	←-----A1----7/8----→
TUE		T(1)	4	2	6		3	←-----A2----7/8----→			
WED	←-----A3----7/8----→			5	1		2	Soft Skills			
THU			4	5	2		1	6	3	T(6)	
FRI			T(5)	←-----A4----7/8----→			6	4	5		
SAT			5	1	6		3				

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|--|--|
| 1. P15MAT31- Engineering Mathematics –III                        | 5. P15EC35 – Network Analysis (KRY)      |
| 2. P15EC32- Analog Electronic Circuits (NMMG)                    | 6. P15EC36 – Signals and Systems (MS)    |
| 3. P15EC33- Digital Electronics Circuits (Mahesh)                | 7. P15ECL37- FET and Op-Amp Circuits Lab |
| 4. P15EC34- Electronic Instrumentation (RM)                      | 8. P15ECL38- Digital Circuits Design Lab |
| 9. P15HU39-**Aptitude and Reasoning Development- BEGINNER (ARDB) |  |

**In charge of Time-table**

**HOD. of E&C Engg. Dept.**

**P.E.S.College of Engineering, Mandya-571401**

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**Department of Electronics & Communication Engg.**

**TIME – TABLE ODD SEM: (Reopening: 25-07-2016 Ending : -11-2016)**

**Semester: III      Section: B      Class Room: ECL- 306      Month: July      Year: 2016**

Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
	MON				5	T E A  B R E A K  L U N C H  T I M E	2		6	1	3
TUE			1	←-----B1----7/8----→			2	3	6		
WED			5	3	4		1	Soft Skills			
THU			3	1	4		6	←-----B2----7/8----→			
FRI		T(1)	4	6	5		2	←-----B3----7/8----→			
SAT	←-----B4----7/8----→			5	2		T(5)				

- |  |  |
|--|--|
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| 2. P15EC32- Analog Electronic Circuits (NMMG)                    | 6. P15EC36 – Signals and Systems (MS)    |
| 3. P15EC33- Digital Electronics Circuits (Mahesh)                | 7. P15ECL37- FET and Op-Amp Circuits Lab |
| 4. P15EC34- Electronic Instrumentation (RM)                      | 8. P15ECL38- Digital Circuits Design Lab |
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**TIME – TABLE ODD SEM: (Reopening: 25-07-2016 Ending : -11-2016 )**

**Semester: V      Section: A      Class Room:ECL- 301      Month: July      Year:2016**

Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON			1	<b>T E A  B R E A K</b>	5	Soft Skills		<b>L U N C H  T I M E</b>	←-----A1----7/8----→		
TUE			4		3	1	6		←-----A2----7/8----→		
WED	←-----A3----7/8----→				5	6	2		4	1	3
THU			T(4)		←-----A4----7/8----→				3	2	6
FRI			3		4	5	6		2	1	
SAT			2		5	4	T(3)				

1. P13EC51-Entrepreneurship Development and Management (MN)
2. P13EC52-Digital Signal Processor and Applications (HRK)
3. P13EC53-Information Theory and Coding (MR)
4. P13EC54-Digital Communication Theory (HSP)
9. P13HU59-Professional and Efficient Avocation-I (PEA-I)

5. P13EC55-Digital CMOS VLSI Design (HSV)
6. P13EC56-Optical Communication Systems (ANA)
7. P13ECL57-Digital Signal Processing Laboratory
8. P13ECL58-Optical and Analog Communication Lab
10. P13EC510-Industry Interaction-II

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**TIME – TABLE ODD SEM: (Reopening: 25-07-2016 Ending : -11-2016 )**

**Semester: V      Section: B      Class Room:ECL- 302      Month: July      Year:2016**

Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON			3	<b>T E A  B R E A K</b>	6	Soft Skills		<b>L U N C H  T I M E</b>	2	1	4
TUE			6		2	4	3		1	5	T(3)
WED					←-----B1----7/8----→				5	6	2
THU			1		5	3	4		←-----B2----7/8----→		
FRI			5		6	3	4		←-----B3----7/8----→		
SAT	←-----B4----7/8----→				1	2	T(4)				

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**In charge of Time-table**

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Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON				T E A B R E A K	←-----A1----7/8----→			L U N C H  T I M E	1	4	3
TUE			2		3	6	5		←-----A2----7/8----→		
WED			4		6	2	3		←-----A3----7/8----→		
THU			Soft Skills		Soft Skills	1	2		5	6	4
FRI			3		4	5	1		←-----A4----7/8----→		
SAT			6		1	5	2				

1. P13EC71-GSM Communication and Networks (BPV)
2. P13EC72- Low Power VLSI Design (LC)
3. P13EC73-Advanced Microcontrollers (BSS)
4. P13EC74-Embedded and Real Time Systems( SS)
5. P13EC75-Elective (Group-B)
6. P13EC76-Elective (Group-C)
7. P13ECL77- Computer Communication Network and VLSI Lab
8. P13ECL78- Embedded and Real Time System Lab
9. Industrial Visit

In charge of Time-table

HOD. of E&C Engg. Dept.

Time Day	7.30 to 8.10	8.10 to 9.05	9.05 to 10.00		10.30 to 11.25	11.25 to 12.20	12.20 to 1.15		2.30 to 3.25	3.25 to 4.20	4.20 to 5.15
MON			4	T E A B R E A K	1	3	6	L U N C H  T I M E	←-----B1----7/8----→		
TUE			5		4	2	3		1	6	
WED			1		5	4	6		2	3	
THU			Soft Skills		Soft Skills	5	3		←-----B2----7/8----→		
FRI					←-----B3----7/8----→				5	4	2
SAT	←-----B4----7/8----→				2	6	1				

1. P13EC71-GSM Communication and Networks (BPV)
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